

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant :	Koichiro Tanaka	Art Unit :	2822
Serial No. :	09/842,797	Examiner :	Maria F. Guerrero
Filed :	April 27, 2001	Confirmation No.:	7383
Title :	METHOD OF FABRICATING SEMICONDUCTOR DEVICE		

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT ACCOMPANYING RCE

Please amend the above-identified application as follows.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 19 of this paper.